

# Homework 1

*Due: Wednesday, February 7, 2024*

All homeworks are due at 11:59 PM on Gradescope.

**Please do not include any identifying information about yourself in the handin, including your Banner ID.**

Be sure to fully explain your reasoning and show all work for full credit.

## Problem 1

There are four dinosaurs: a tyrannosaurus rex, a velociraptor, a brachiosaurus, and a spinosaurus. Each of the dinosaurs has very specific sleeping habits depending on its habits and lifestyle. There are very strict rules about when each dinosaur sleeps:

- i. At least one of the tyrannosaurus rex and the velociraptor is sleeping.
- ii. If the tyrannosaurus rex is sleeping, then the brachiosaurus is sleeping.
- iii. If the velociraptor is not sleeping, then the brachiosaurus and the spinosaurus are sleeping or the tyrannosaurus rex is sleeping.

Complete the following questions using the above information about the dinosaurs:

- a. For each dinosaur, assign a propositional variable to represent the proposition that it is sleeping. Using these variables and the connectives  $\wedge$ ,  $\vee$ ,  $\rightarrow$ , and  $\neg$ , translate the rules (i)-(iii) above into propositional logic.
- b. We can add to the list above a fourth rule *that uses implication* which, together with the first three, guarantees that the brachiosaurus is always sleeping. Write such a rule in words (like we did for (i)-(iii)), then write its translation in propositional logic.

Explain why adding this rule guarantees that the brachiosaurus is sleeping. What proof rules from lecture 4 did you use in this argument? (For example: and introduction, or elimination, modus ponens. . .?)

- c. Represent the following sentence as a formula of propositional logic: “if the tyrannosaurus rex and the brachiosaurus are sleeping, then the spinosaurus is not sleeping.” Under what conditions is this sentence *false*? Why?

## Problem 2

In this problem, we'll define and investigate the properties of a new logical connective, XOR, "exclusive or." XOR is a popular connective among computer scientists, especially those who design physical circuits, but it's neglected by mathematicians. We'll use the notation  $\oplus$  for this connective.

(Note: you shouldn't use this connective on any CS22 assignments or assessments outside this problem, unless it's explicitly mentioned. We wear mathematician hats in this class.)

The connective  $\oplus$  is defined to have the behavior that, given propositions  $p$  and  $q$ , we have that  $p \oplus q$  is true if and only if *exactly* one of  $p$  or  $q$  is true (not both!). Otherwise,  $p \oplus q$  is false.

- a. Write down two natural language English sentences that are sensibly translated to propositional logic using  $\oplus$  and at least one more connective. Then write down the propositional logic translation of each English sentence. You should choose two sentences that don't translate to the same propositional formula!

You may use any atomic propositions you'd like, but please tell us which natural language propositions correspond to which propositional letters.

- b. Let  $p$  and  $q$  be propositions. *Using only conjunction and negation*, write a propositional formula that is equivalent to  $p \oplus q$ . Verify this equivalence using truth tables. (You may want to use Lean to generate a truth table for your formula.)
- c. The *elimination rule* for  $\oplus$  tells us how we can *use* the information that an XOR statement is true. Here is the elimination rule, written in natural language:

If you know  $p \oplus q$  and your goal is  $g$ , then in order to prove your goal, it suffices to show  $(p \wedge \neg q) \rightarrow g$  and  $(\neg p \wedge q) \rightarrow g$ .

An *introduction rule* for  $\oplus$  gives sufficient conditions to prove a goal of the form  $p \oplus q$ . What are the introduction rule(s) for  $\oplus$ ? There may be more than one! You can write the rules in natural language, as we've written the elimination rule above.

You may find it helpful to take inspiration from the elimination rule above and the introduction rules for disjunction ( $\vee$ ).

## Problem 3


This problem is a Lean question!

This homework question can be found by navigating to `BrownCs22/Homework/Homework01.lean` in the directory browser on the left of your screen in your Codespace. The comment at the top of that file provides more detailed instructions.

You will submit your solution to this problem separately from the rest of the assignment. Once you have solved the problem, download the file to your computer (right-click on the file in the Codespace directory browser and click “Download”), and upload it to Gradescope.

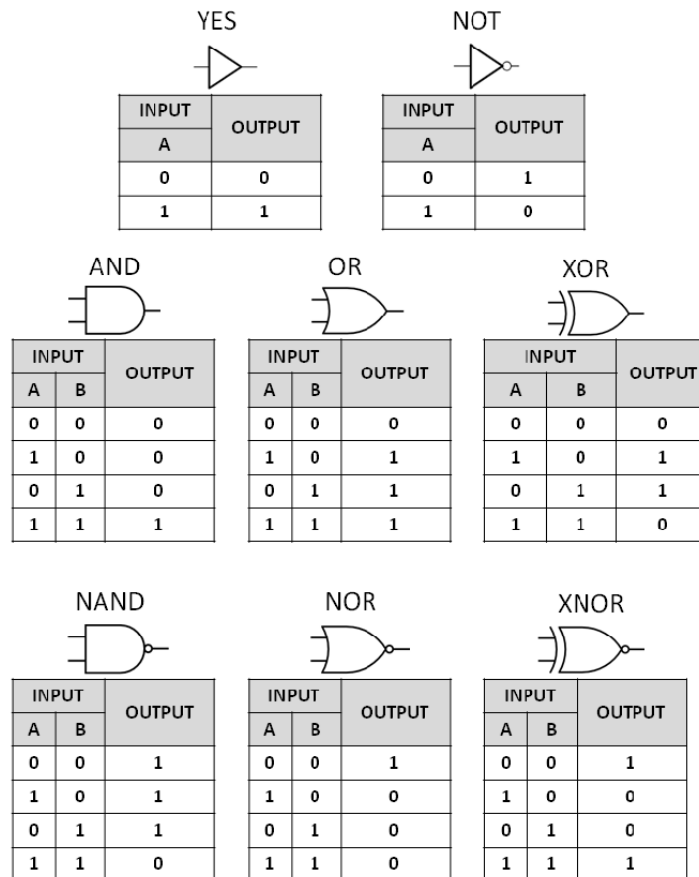


## Problem 4 (Mind Bender — *Extra Credit*)

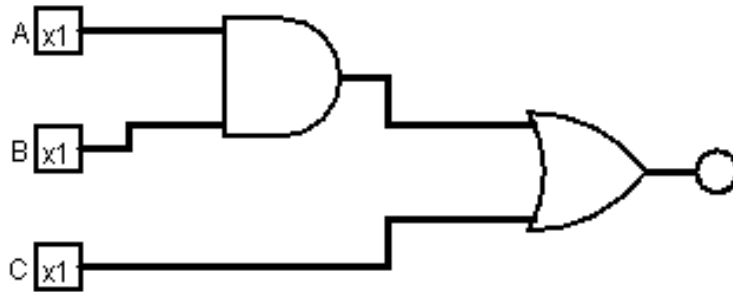
*Mind Benders* are extra credit problems intended to be more challenging than usual homework problems. They are denoted with a  symbol. Occasionally, some parts from problem 1-3 might also be extra credit problems.

These problems are sometimes quite challenging. Try them on your own—don't look to the TAs for too much help!

One application of propositional logic is in the design of logic circuits, which allow computers to perform operations on the binary data that they work with. Often in computers, this is accomplished by using transistors, but for our modeling purposes we can use the following notation.



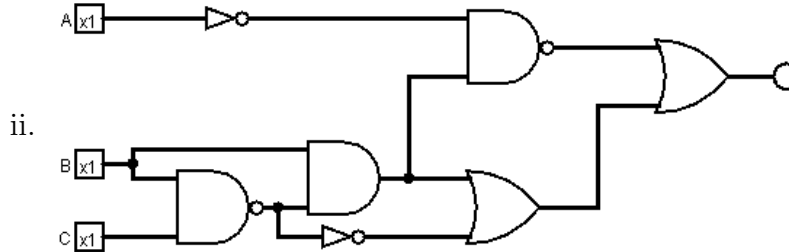
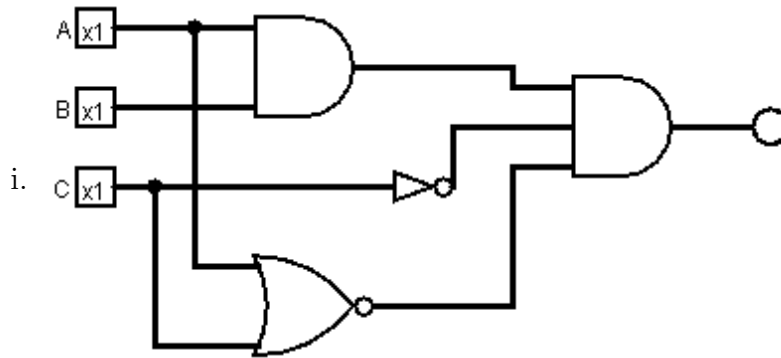
For example, we can represent the proposition  $(A \wedge B) \vee C$  as the circuit



a. Draw circuits corresponding to the following propositions.

- i.  $(A \vee B) \wedge C$
- ii.  $A \wedge ((A \wedge B) \vee (B \wedge C))$
- iii.  $(\neg(A \wedge B) \vee (\neg A \vee C)) \vee (B \wedge C)$

b. Write the following logic circuits as logic statements and create a truth table for each.



c. The NAND and NOR gates are also known as the universal logic gates since we can implement any of the other gates by just using the chosen universal gate.

- i. Implement AND, OR, and NOT by only using NAND gates.
  - ii. Why can it be helpful to use universal logic gates in computers rather than all of the gates separately?
- d. Logic circuits can also be used to perform Boolean arithmetic. A simple example is an *addition circuit*, which takes in 2 one-digit Boolean numbers (representing 1 as true and 0 as false) and returns their sum. Draw a circuit that has this behavior.

**HINT:** Notice that while each input is only one Boolean digit, your output could have two! (We represent 2 as 10 in binary.) Therefore, utilizing two outputs, one for each digit of the result, can be a useful strategy. It can also be useful to create a truth table for Boolean addition before making the circuit.